The IEEE standards-based MAC with traffic prioritization from DornerWorks performs high-accuracy time synchronization “behind the scenes.”

IEEE STANDARDS-BASED MAC WITH TIME SYNCHRONIZATION

This deterministic ethernet IP provides low latency and high throughput, support for diverse traffic inputs over a common network with gapless scheduling.

Supercharge your time-reliant products and systems with this high-accuracy solution!

DornerWorks.com | +1.616.245.8369

RIGOROUS STANDARDS
- IEEE 802.3 - 1 Gbps full-duplex
- IEEE 802.1AS-REV slave profile
- IEEE 802.1Q-2014 - FOTSS strict priority or credit-based scheduler for up to 8 QoS

HIGH ACCURACY
- Synchronizes to a grandmaster with +/- 8-ns accuracy (double hop)
- Rapid time convergence

VERSATILE INTERFACES
- Xilinx AMBA/AXI4 Lite and Stream interfaces
- MDIO and RGMII or SGMII interfaces to PHY
FROM ALPHA TO BETA in the time frame and budget they promised.

FPGA EXPERTISE COST-EFFECTIVE TIME SYNC

Add time-synchronization and high-speed networking capabilities to legacy devices, or build a fleet of new networked products from the ground up!

DornerWorks.com | +1.616.245.8369