This demonstrates DornerWorks design and implementation of software controllable FPGA-based networked video systems on a Xilinx Zynq UltraScale+ MPSoC ZCU102/106.

This hardware accelerates designs for video conferencing, surveillance, Advanced Driver Assisted Systems (ADAS), and streaming and encoding applications.

Xilinx Zynq UltraScale+ MPSoC & DornerWorks Engineering FPGA-BASED VIDEO STREAMING

DornerWorks Multi-Stream Display Port demo platform is intended for Xilinx FAE/TSE teams and mutual customers.

This demo has been shown at multiple Xilinx events, including the international sales conference, Xilinx Developers Forum, and Xilinx Security Working Group.

We welcome your feedback regarding customers that would have interest in the demo capabilities like Video Codec Unit (VCU), PCIe, Time-Sensitive Networking (TSN), or Machine Learning (ML).

Soon to feature...
- HD-SDI
- Custom FMC
- VCU
- AVB
- and more

Future roadmap
- Machine Learning enabled by DPU

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DornerWorks is a professional, flexible, AND AN ESSENTIAL PART OF OUR TEAM

With a time-synchronous video streaming solution, you can lead the market by delivering the real-time results your customers deserve.

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Time-critical video streaming, enabled by Xilinx IP

- Xilinx Video Mixer v3.0
- Xilinx Video Processing Subsystem v2.0
- Xilinx Video Test Pattern Generator v8.0
- Xilinx HDMI Transmitter Subsystem v3.1
- Xilinx Video PHY Controller v2.2
- Xilinx DisplayPort RX Subsystem v2.0

DornerWorks FPGA Video Demo

Xilinx ZCU102/106

Dataflow IN
- Display Port MST
- Video PHY
- Display Port Controller

Dataflow OUT
- HDMI Transmitter
- HDMI PHY
- HDMI Out

Data Processing
- VPSS 1
- VPSS 2
- VPSS 3
- VPSS 4

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Schedule A Consultation

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